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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/008,272	11/13/2001	Benjamim Tang	35706.4500	2550
34398	7590	12/29/2004	EXAMINER	
PRIMARION 2507 WEST GENEVA DRIVE TEMPE, AZ 85282			BURD, KEVIN MICHAEL	
			ART UNIT	PAPER NUMBER
			2631	

DATE MAILED: 12/29/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/008,272

Applicant(s)

TANG, BENJAMIM

Examiner

Kevin M. Burd

Art Unit

2631

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 August 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-8 is/are allowed.
- 6) ☒ Claim(s) 9-16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

1. This office action, in response to the amendment filed 8/19/2004, is a non-final office action.

Response to Arguments

2. Applicant's arguments, see remarks filed 8/19/2004, with respect to claims 1-8 have been fully considered and are persuasive. The rejections of these claims have been withdrawn.
3. Applicant's arguments, see remarks filed 8/19/2004, with respect to the rejections of claims 9-16 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, new grounds of rejection are made in view of Millar (US 6,337,590).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 9-16 are rejected under 35 U.S.C. 102(e) as being anticipated by Millar (US 6,337,590).

Regarding claims 9, 12 and 16, Millar discloses a delay locked loop device and method of using the DLL. Figure 3 discloses a phase detector for comparing a reference signal and a clock signal. This phase detector will output early and late pulse to the clock jitter filter 29 (column 5, line 63 to column 7, line 7). The clock jitter filter 29 comprises a pulse accumulator 29A and a clock divider 29B as shown in figure 4. The POS and NEG outputs of the phase accumulator 29A represents the accumulation of phase information over an interval defined by the clock divider 29B (column 7, lines 48-52). The clock divider will divide a clock and thereby provide a scaled down accumulation of the pulse signals. The clock jitter filter will output signals to control circuitry 31. This control circuitry is the "integrator" since it will provide a control signal for use in aligning a clock signal to a data signal. This is shown in figure 3. The control circuitry controls the digital delay line, which provides a signal to the phase detector 25.

Regarding claims 10 and 11, early and late signals are generated as shown in figure 5.

Regarding claims 12-15, the phase and frequency of the clock signal is adjusted in the digital delay line 13 and provided as feedback to the phase detector 25.

Allowable Subject Matter

Claims 1-8 are allowed.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Marko et al (US 5,463,351) discloses a phase detector, pulse accumulator receiving early/late signals and a phase adjustment circuit coupled to a VCO that provides feedback to the phase detector as shown in figure 4. Marko does not disclose the pulse accumulator providing a scaled down pulse accumulator.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Burd whose telephone number is (571) 272-3008. The examiner can normally be reached on Monday - Thursday 9 am - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on (571) 272-3021. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kevin M. Burd

12/21/2004

KEVIN BURD
PRIMARY EXAMINER